

CSCI 4907/6545 Software Security

Fall 2025

Instructor: Jie Zhou

Department of Computer Science

George Washington University



Slides materials are partially credited to Gang Tan of PSU.

Announcements

- Assignment 1 due today
- Assignment 2 to be released today
- (Mini-) Assignment 1.5: Prepare at least one question about anything covered so far for the next lecture

Limitations of ret2libc Attacks

- On AMD64 (and many other arch, e.g., AArch64), function arguments are first passed via registers instead of stack.
- Limited exploitable functions
 - `system()` and other “profitable” library functions could be removed.
- Can only execute straight-line code
 - Desired malicious computation may be invalidated by functions themselves.

**What really matters are the instructions
and how they are arranged.**

Return-oriented Programming (ROP)

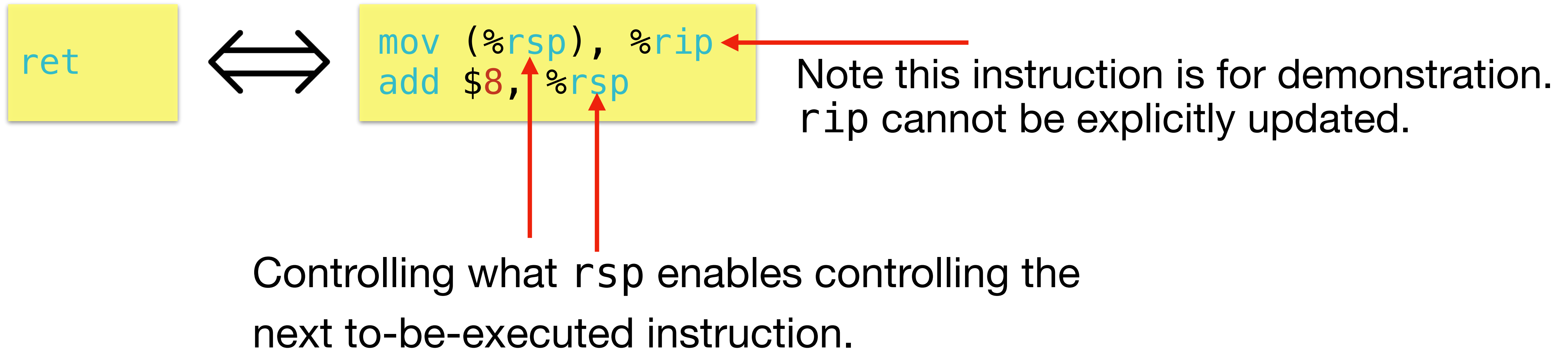


An exploit technique that allows arbitrary code execution without calling any functions.

- Exploiting memory corruption bugs
 - Often starting with a corrupted return address
- Chaining code sequences, called *gadgets*, that end with a `ret`
 - Generally, gadgets ending with control flow transfer instructions, e.g. `jmp`
- Turing-complete
 - Memory operations
 - Arithmetic and logic
 - Control flow

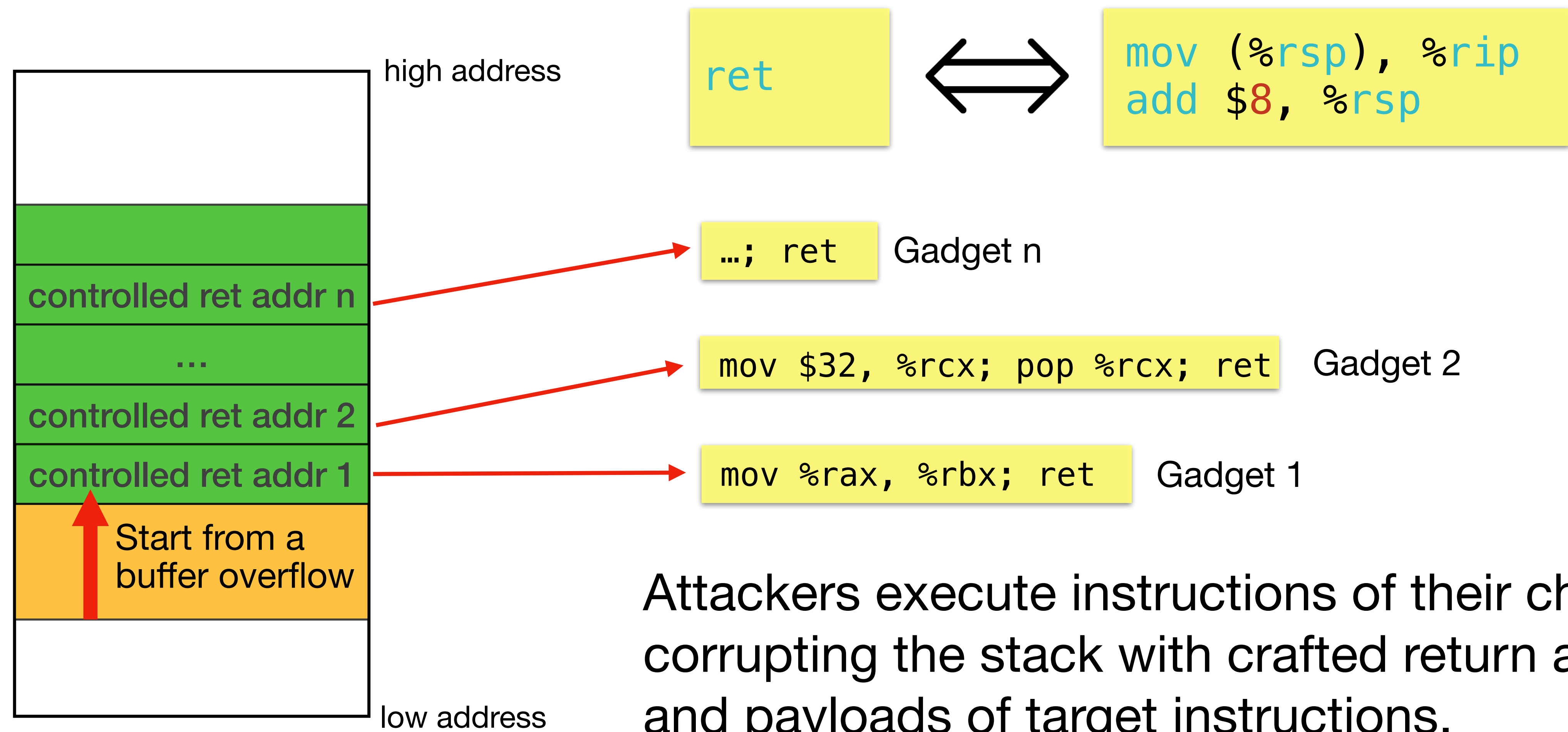
Return-oriented Programming (ROP)

- Use `ret` to jump to the “profitable” instructions to the attacker’s interest



- Use `rsp` as a confused deputy for `rip`
 - Attackers use `rsp` to control the flow of the victim program.

Chaining Multiple ret

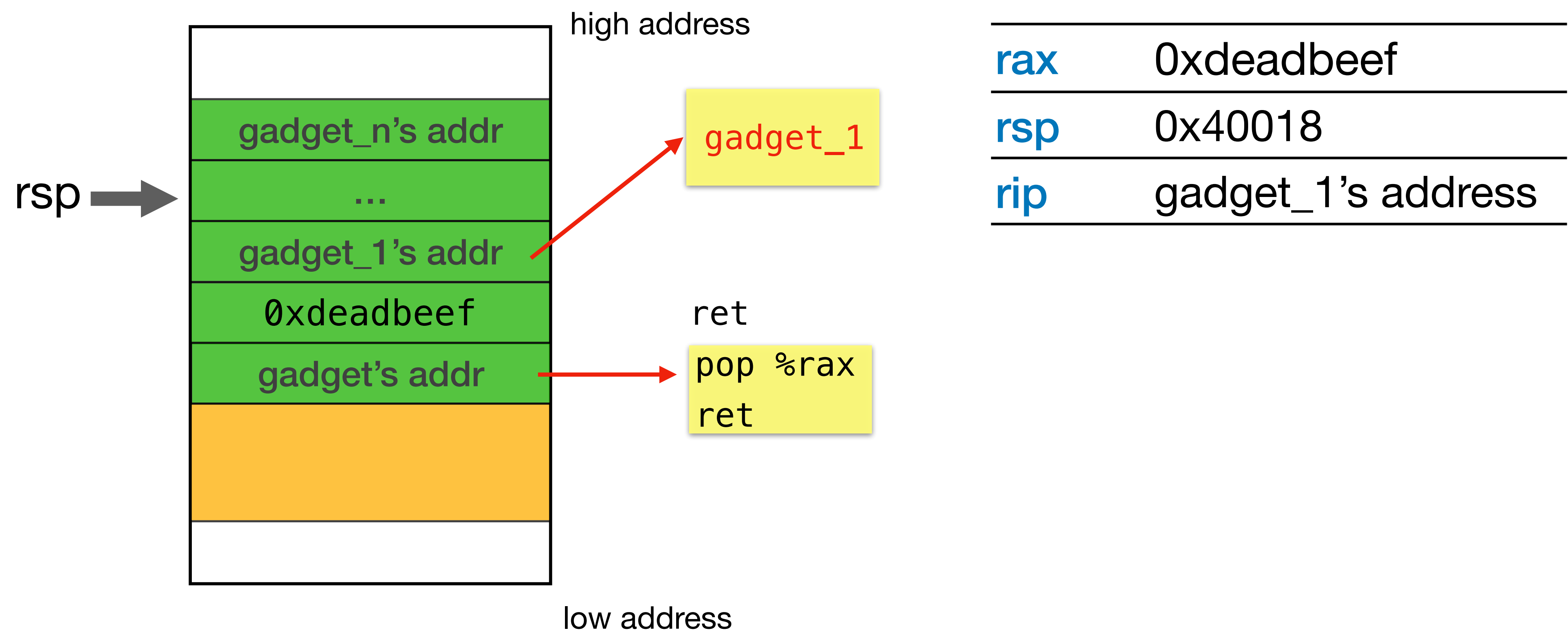


Attackers execute instructions of their choosing by corrupting the stack with crafted return addresses and payloads of target instructions.

Loading a Constant

💡 How to load an arbitrary constant (e.g. 0xdeadbeef) into a register?

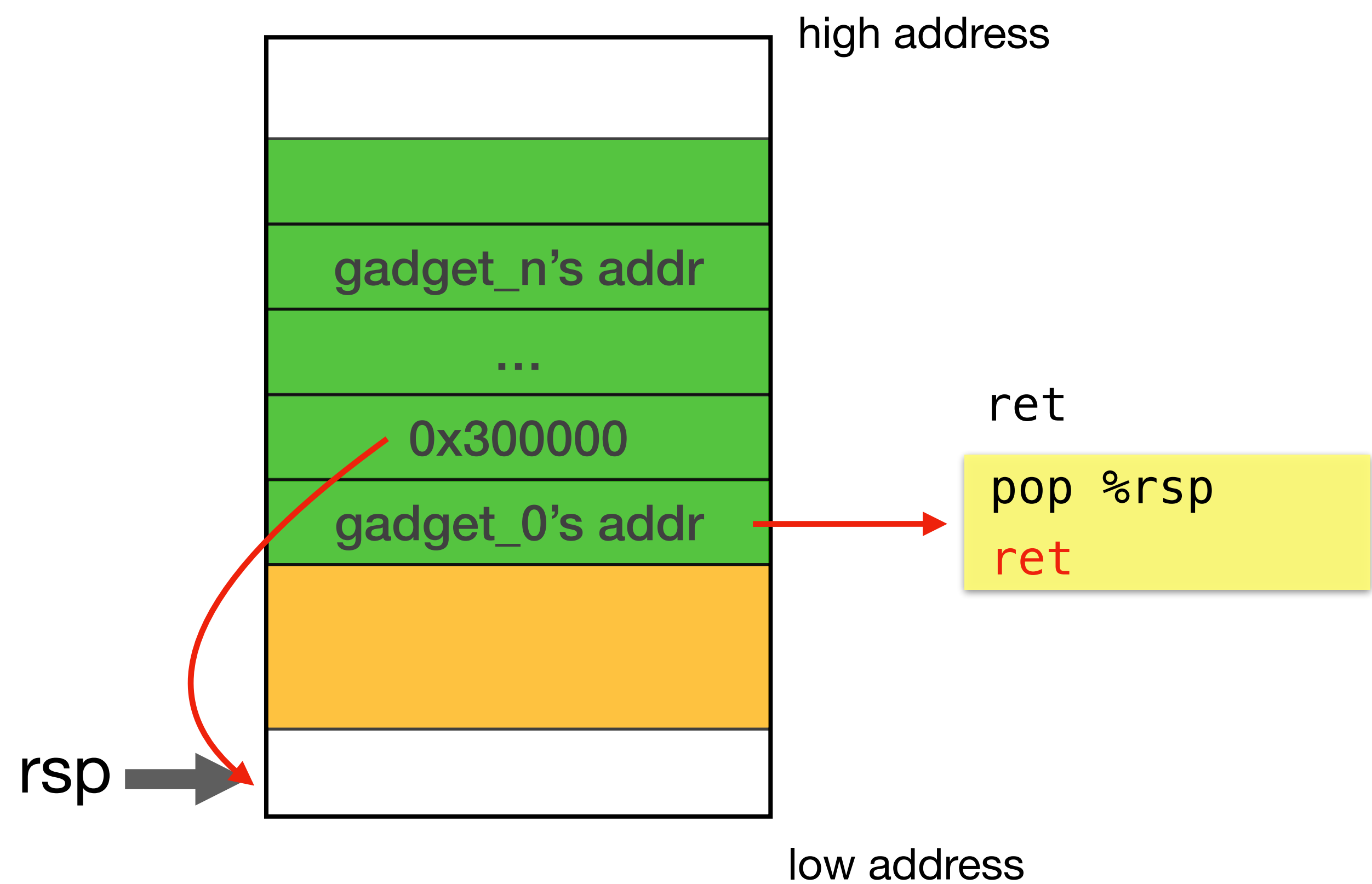
- Option 1: Pop the constant to the target register



Stack Pivoting

💡 How to enable a larger “stack”?

- Pop the target address (e.g. 0x300000) to `rsp`, and `ret`.



<code>rsp</code>	<code>0x300000</code>
<code>rip</code>	<code>gadget ret's address</code>

`pop %rsp` is special:

- `rsp` gets incremented by one word.
- Data pointed by old `rsp` is loaded to `rsp`.

ROP Gadgets Are Abundant

ROP gadgets: Instructions sequences ending with a `ret`.

- Linked libraries provide a plethora of instructions.
- x86 ISA uses variable-length instructions.
 - Allows *unintended* instruction sequences

`ret` is encoded as `0xc3` in hexadecimal format.

Starting one byte later, the attacker instead obtains

f7 c7 07 00 00 00	test \$0x00000007, %edi
0f 95 45 c3	setnzb -61(%ebp)

c7 07 00 00 00 0f	movl \$0x0f000000, (%edi)
95	xchg %ebp, %eax
45	inc %ebp
c3	ret

ROP Thesis

The Geometry of Innocent Flesh on the Bone: Return-into-libc without Function Calls (on the x86),
by Hovav Shacham.

“In any sufficiently large body of x86 executable code there will exist sufficiently many useful code sequences that an attacker who controls the stack will be able, by means of the return-into-libc techniques we introduce, to cause the exploited program to undertake arbitrary computation.”



Also true in almost all other major architectures.

Buffer Overflows

- Stack overflow: overflowing a memory region on the stack (e.g., overwriting a return address)
- Heap overflow: overflowing a memory region dynamically allocated on the heap

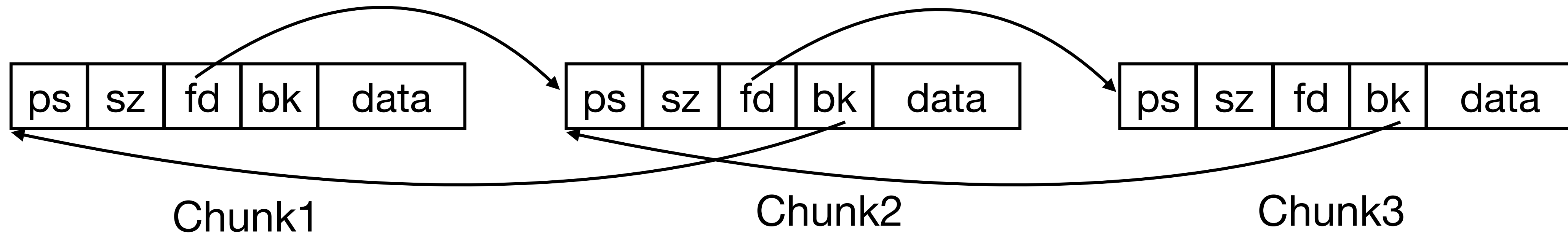
Heap Overflows

Example Heap Allocator

- Maintain a doubly-linked list of allocated and free chunks
- `malloc()` and `free()` modify this list

```
struct chunk {  
    size_t    ps;        // prev_size  
    size_t    sz;        // size  
    chunk_t *fd;        // forward pointer  
    chunk_t *bk;        // backward pointer  
    unsigned char data[]; // allocated space for user data  
};
```

Example Heap Allocator



- ps: prev_size
- sz: size
- fd: forward pointer
- bk: backward pointer
- data: allocated space for user data

Temporal Memory Safety

Root Cause

***Manual* Memory Management**

Format String Vulnerabilities

